



Aruba Certified Switching Associate Dumps

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QUESTION 1

Which Ethernet port combination does the Aruba 5400R ZL2 switch series support?

- A. 10/100 Megabit Ethernet, Gigabit Ethernet, and 100 Gigabit Ethernet
- B. Gigabit Ethernet, 25 Gigabit Ethernet, and 40 Gigabit Ethernet
- C. 10/100 Megabit Ethernet, Gigabit Ethernet, 25 Gigabit Ethernet, and 100 Gigabit Ethernet
- D. 10/100 Megabit Ethernet, Gigabit Ethernet, and 40 Gigabit Ethernet

Correct Answer: D

Reference: https://www.arubanetworks.com/assets/ds/DS_5400Rzl2SwitchSeries.pdf

QUESTION 2

Refer to the exhibit.

VLAN	Name	Mode	Mapping
1	DEFAULT VLAN 1	native-untagged	port
10	Sales	trunk	port
20	Service	trunk	port
30	Customer	trunk	port

Which command produces the above output?

A. show interface port 1/1/10 trunk

B. show vlan port 1/1/10

C. show trunk port 1/1/10 extended

D. show interface port 1/1/10 switchport

Correct Answer: C

QUESTION 3

You have completed a successful VSF stack between two switches.

Without manually assigning a backup switch, what is the expected output when issuing a show vsf command?



 <omitted></omitted> Mbr Mac Address type Stat 1 88:3a:30:a3:e5:40 JL660A Com 2 88:3a:30:a6:d9:80 JL660A Seco © B. T1-6300-A# show vsf <omitted></omitted> Mbr Mac Address type Stat 1 88:3a:30:a3:e5:40 JL660A Prim 2 88:3a:30:a6:d9:80 JL660A Seco 	mande <mark>r</mark>			
 1 88:3a:30:a3:e5:40 JL660A Com 2 88:3a:30:a6:d9:80 JL660A Second T1-6300-A# show vsf <omitted></omitted> Mbr Mac Address type State 1 88:3a:30:a3:e5:40 JL660A Prime 	mander			
 B. T1-6300-A# show vsf <omitted></omitted> Mbr Mac Address type Stat 1 88:3a:30:a3:e5:40 JL660A Prime 	indury			
Mbr Mac Address type Stat 1 88:3a:30:a3:e5:40 JL660A Prim				
1 88:3a:30:a3:e5:40 JL660A Prim				
2 88:3a:30:a6:d9:80 JL660A Seco				
	ndary			
C. T1-6300-A# show vsf <omitted></omitted>				
Mbr Mac Address type Stat				
1 88:3a:30:a3:e5:40 JL660A Mast				
2 88:3a:30:a6:d9:80 JL660A Mem	nber			
D. T1-6300-A# show vsf <omitted></omitted>				
Mbr Mac Address type Stat	us ID			
1 88:3a:30:a3:e5:40 JL660A Mast				
2 88:3a:30:a6:d9:80 JL660A Stan	ter			

A. Option A

B. Option B



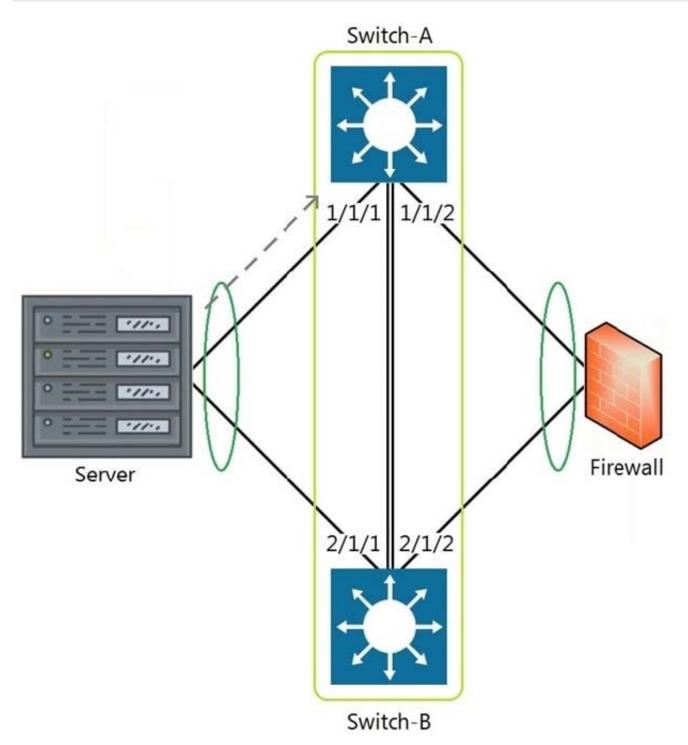
- C. Option C
- D. Option D

Correct Answer: D

QUESTION 4

Refer to the exhibit.





The above scenario shows a packet from the Server destined for the Firewall. Switch-A and Switch-B are bundled as VSF stack. The LAG between the VSF stack and the firewall indicates a hash function to forward the packet on port 2/1/2. Which statement is true regarding how Switch-A will forward the packet?

A. Switch-A will forward the packet on port 1/1/2. VSF will override the typical LAG hash function used for the physical interface selection.

B. Switch-A will drop the packet. Multi-Chassis lag to multi-chassis lag is not a supported feature of VSF.

C. Switch-A will encapsulate the packet using GRE to forward to Switch-B in order for the packet to egress on port 2/1/2 per the hash function.



D. Switch-A will forward the packet along the VSF link to Switch-B so that it will egress on port 2/1/2 per the hash function.

Correct Answer: D

QUESTION 5

DRAG DROP

Match each description to the correct term.

Select and Place:

Term	Description
ASIC	receives and sends frames by using
Control Plane	Application-Specific Integrated Circuits (ASICs)
Data Plane	switches packets faster than using software
Management Plane	determines packet forwarding using routing, switching, security, and flow optimization
	handles switch monitoring

Correct Answer:

Term		Description
	Data Plane	receives and sends frames by using Application-Specific Integrated Circuits (ASICs)
	ASIC	switches packets faster than using software
3 <u></u>	Control Plane	determines packet forwarding using routing, switching, security, and flow optimization
	Management Plane	handles switch monitoring

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