

HP2-T16^{Q&As}

Industry Standard Architecture and Technology

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QUESTION 1

Which switch port type is required to support FC-AL devices in a SAN?

A. U_Port

B. E Port

C. FL_Port D. NL_Port

Correct Answer: D

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QUESTION 2

What are advantages of DAS solutions?

A. maximum scalability

B. ease of deployment

C. low intial cost

D. snapshot capability

Correct Answer: BC

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QUESTION 3

What must you check prior to adding another processor to an existing system. (Select three)

A. amount of memory in the system

B. compatibility of the new process with existing processors

C. firmware requirements for the new processor

D. number of users currently logged into the system



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E. number of processors the operating system supports

F. weight of the new processor

Correct Answer: BCE

QUESTION 4

You have a disk enclosure with 14 x 72GB Wide-Ultra2 SCSI hard disks connected. You add an additional disk enclosure with 8 x 146GB Hot-Pluggable Ultra320 SCSI hard disks to an array controller. To optimize performance, you move 3 x

72GB Ultra2 hard disks to a new enclosure to balance the number of hard disks across two channels.

The disks in the new enclosure are not working correctly and the disks are not seen in the array controller management tool. There are no conflicting device IDs and termination is correct. All enclosures were powered on before powering up

the server. What are the possible causes of the problem? (Select two)

- A. Ultra320 SCSI devices cannot be mixed with Wide-Ultra2 SCSI devices.
- B. You must update the SCSI Controller BIOS to the latest supported revision.
- C. The cables and/or connectors are faulty or not properly seated.
- D. The number of disks exceeds the capability of the controller.
- E. The SCSI I/O module on the disk enclosure is faulty.

Correct Answer: BD

QUESTION 5

In systems with AMD processors, what allows communication between processors and the I/O subsystem?

- A. Northbridge
- B. Southbridge
- C. HyperTransport link
- D. APIC
- E. QuickPath Interconnect

Correct Answer: C

{\rtf1\ansi\ansicpg936\deffo\deffang1033\deflangfe2052{\fonttbl{\fo\fnil\fcharset0 MS Shell Dlg 2;}} \viewkind4\uc1\pard\lang2052\fo\fs17 Industry Standard Architecture - Student Guide 1 - Page 37\par AMD processors are able to communicate with each other through HyperTransport point-to-point links. This enables one processor to access the memory connected to another processor. Inside the processor, a crossbar switch connects the processor, memory controller, and HyperTransport links.\par AMD processors also use HyperTransport links to connect to the I/O subsystem.\par The links on particular processors are connected to I/O tunnels that support the I/O devices.\par All



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other processors can communicate with the I/O system through the HyperTransport links.\par Legacy devices are also connected to one of the I/O tunnels.\par $\}$

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