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QUESTION 1

When using the default ARM tool-chain libraries for bare-metal applications. I/O functionality is rerouted and handled by a connected debugger. This is often referred to as semihosting. Which one of the following explanations BEST describes how this feature can be implemented by a debugger?

- A. The library directly sends I/O requests to the debugger using the JTAG connection
- B. While the target is running, the debugger processes I/O requests from a shared queue in memory
- C. The I/O library calls rely on an Ethernet connection to redirect the requests to the debugger
- D. The I/O library calls generate an exception that is trapped and handled by the debugger

Correct Answer: D

QUESTION 2

When timing a critical function for an algorithm, using platform time functions such as get time of day (), the result is unpredictable; there is significant variance in the measured time between different runs of the benchmark. Which of the following strategies would improve the accuracy of the measurement?

- A. Time multiple executions of the algorithm and average the result
- B. Break the algorithm into smaller pieces and time them individually
- C. Run the code on a software model of the platform and collect the results on that system
- D. Add some code with a known overhead to the algorithm to make it run slower, and remove the overhead afterwards

Correct Answer: A

QUESTION 3

Under which of the following circumstances is TLB maintenance always required?

- A. If a TLB miss occurs
- B. On every process switch
- C. If the TLB reports a fault
- D. When a page table entry is changed

Correct Answer: D

QUESTION 4

Which of the following is TRUE for dynamically linked executables?



- A. They can contain unresolved relocations
- B. They contain the code and data for all libraries they use
- C. They are larger than an equivalent statically linked application
- D. They are designed to be run standalone with no other supporting software

Correct Answer: A

QUESTION 5

In an ARMv7 processor that includes the Advanced SIMD (NEON) extension, how many single precision floating point values can be stored in the Q0 register?

- A. 1
- B. 2
- C. 4
- D. 8

Correct Answer: C

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