EN0-001^{Q&As}

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QUESTION 1

The automatic removal of a cache line from a cache to free the location is known as cache line:

A. Coherency

B. Pre-fetch

C. Eviction

D. Allocation

Correct Answer: C

QUESTION 2

The Cortex-A9 processor has 6 breakpoint units and 4 watchpoint units. What is the maximum number of breakpoints the debugger can set on code in ROM?

A. 6

B. 10

C. 2

D. The debugger can use the BKPT instruction to do this.

Correct Answer: A

QUESTION 3

The effect of clicking the Stop button in a debugger is to:

A. Put the processor(s) into debug state.

B. Force the processor to execute a BKPT instruction

C. Hold the processor in a Reset condition

D. Re-initialize the memory contents.

Correct Answer: A

QUESTION 4

How is data written into NOR flash memory?

A. Data can only be written once, when the flash device is being manufactured

B. Writing data to the memory locations using store instruction, as you would with RAM C. Reading and writing specific registers following a device-specific procedure

D. Using an external programming device, which utilizes an ultra-violet lamp to alter the data stored on the device

Correct Answer: C

QUESTION 5

When linking with the standard C library, which library functions MUST be redefined in order to port your code to a new piece of production hardware?

A. Functions that are not compliant with the ISO C library standard

B. Functions that are not compliant with the 1985 IEEE 754 standard for binary floating-point arithmetic

C. Target-dependent functions which use semihosting

D. Functions called implicitly by the compiler

Correct Answer: C