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QUESTION 1

An application contains three calls to an external function, `foobar()`, which is defined in a shared (or dynamic) library. How many copies of `foobar()` will the linker place in the application image?

(Ignore linker inlining)

- A. None
- B. Always one
- C. Always three
- D. One or more depending on optimization level

Correct Answer: A

QUESTION 2

The interval of time from an external interrupt request signal being raised to the first fetch of an instruction of the interrupt handler is called the interrupt:

- A. Latency
- B. Priority
- C. Service thread
- D. Jitter

Correct Answer: A

QUESTION 3

In which of the following scenarios would cache maintenance operations be necessary in an ARMv7 system?

- A. Before executing code that uses the NEON instruction set
- B. Before handling an interrupt request raised by an external device
- C. Before checking the status of a semaphore
- D. Before reading cacheable memory that has been written to by an external bus master

Correct Answer: D

QUESTION 4

Which THREE of the following items should be preserved by software when entering dormant mode? (Choose three)



- A. Current Program Status Register (CPSR)
- B. Contents of the Level 2 data cache
- C. The Floating Point Status and Control Register (FPSCR)
- D. All User mode general-purpose registers
- E. The CP15 Multiprocessor Affinity Register
- F. Contents of the Level 1 data cache

Correct Answer: ACD

QUESTION 5

Which of the following is an advantage of the single-step debug technique?

- A. It allows a complete trace of real-time program execution to be captured
- B. It reduces the number of pins required to connect the debugger to the processor
- C. It allows examination of the system state before and after execution of a statement
- D. It requires only one change to the program source code

Correct Answer: C

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