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QUESTION 1

LDREX and STREX were introduced in which ARM architecture version?

- A. ARMv5TE
- B. ARMv6
- C. ARMv6K
- D. ARMv7

Correct Answer: B

QUESTION 2

The Memory Protection Unit (MPU) of Cortex-R4 performs which of the following tasks?

- A. Translates virtual addresses to physical addresses
- B. Generates parity information to detect soft errors in memory
- C. Performs access permission checks
- D. Permits the system to be divided into secure and normal worlds, through the use of ARM's TrustZone technology

Correct Answer: C

QUESTION 3

A C code segment contains three calls to a function, foobar ().

This code segment is to be linked with a static library that defines foobar ().

Ignoring inlining, how many copies of foobar () will the ARM linker place in the output?

- A. None
- B. Always one
- C. Always three
- D. One or more depending on optimization level

Correct Answer: B

QUESTION 4

Which of the following processor resources do NOT have to be saved or modified by the Linux scheduler during context



switch?

- A. Registers R0-R15
- B. Thread and process ID registers
- C. The CPSR
- D. NEON and VFP registers

Correct Answer: D

QUESTION 5

Assume a Big-Endian (BE) memory system with the following memory contents. Byte Address Contents 0x100 0x11 0x101 0x22 0x102 0x33 0x103 0x44 If R5 = 0x100, what are the contents of R4 after performing the following operation? LDR R4, [R5]

- A. 0x11223344
- B. 0x44332211
- C. 0x22114433
- D. 0x33441122

Correct Answer: A

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