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QUESTION 1

Which ARMv7 instructions are recommended to implement a semaphore?

- A. SWP, SWPB
- B. TEQ, TST
- C. STC, SBC
- D. LDREX, STREX

Correct Answer: D

QUESTION 2

Processors which implement the ARMv7-A architecture can be configured to allow unaligned memory access. Unaligned accesses have a number of advantages, disadvantages, and limitations.

Which TWO of the following statements are true? (Choose two)

- A. Unaligned accesses may take more cycles to execute than aligned accesses
- B. Unaligned loads and stores are necessary for accessing fields in packed structures
- C. A program compiled using unaligned accesses can be safely executed on all ARMv7-A devices
- D. If the relevant control register setting is enabled all loads and stores can function from unaligned addresses
- E. Unaligned accesses can only be made to Normal memory

Correct Answer: AE

QUESTION 3

What is an "Entry point" in an application?

- A. A place where execution can start
- B. The location of the main () function
- C. The lowest address contained in a program image
- D. A location where the linker can store additional information

Correct Answer: A

QUESTION 4



According to the AAPCS, how many bytes are used to store a C variable of type `\\int\\` in memory?

- A. 1 byte
- B. 2 bytes
- C. 4 bytes
- D. 8 bytes

Correct Answer: C

QUESTION 5

Under which of the following data-sharing scenarios would cache maintenance operations be necessary?

- A. Sharing data with another thread running on the same core
- B. Sharing data with another process running on the same core
- C. Sharing data with an external device
- D. Sharing data with another CPU in an SMP system

Correct Answer: C

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